

# J/10-13-00  
V-Jones

**S/N Unknown**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Judy Lynn Westby	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	934.007US1
Title:	METHOD AND APPARATUS FOR USING CRC FOR DATA INTEGRITY IN ON-CHIP MEMORY		

**PRELIMINARY AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Please amend the specification on page 1, after line 11, to add:

-- This application is related to the following applications filed on even data herewith, each of which is incorporated by reference: "Method and Dedicated Frame Buffer for Loop Initialization and Responses" by Judy Lynn Westby, <sup>Ser. No. 09/193,337</sup> "Method and Dedicated Frame Buffers for Receiving Frames" by Judy Lynn Westby, <sup>Ser. No. 09/193,681</sup> and "Method and Apparatus to Reduce Arbitrated-Loop Overhead" by Judy Lynn Westby and Michael H. Miller. <sup>Ser. No. 09/193,482</sup>

Respectfully submitted,

JUDY LYNN WESTBY

By her Representatives,

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Date 17 Nov 1998 By Charles A. Lemaire  
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Date of Deposit: 17th November 1998  
I hereby certify that this paper or fee is being deposited with the United States Postal Service as "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231. Chris Hammond  
(Name)

Chris Hammond  
(Signature)

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